



Ref No : IJSDR / Vol 1 / Issue 5 / 133

To,
D. Vijayalakshmi

Subject: Publication of paper at International Journal of Scientific Development and Research.

Dear Author,

With Greetings we are informing you that your paper has been successfully published in the International Journal of Scientific Development and Research (ISSN: 2455-2631). Following are the details regarding the published paper.

About IJSDR : ISSN Approved - International Scholarly open access, Peer-reviewed, and Refereed Journal, Impact Factor: 9.15, (Calculate by google scholar and Semantic Scholar | AI-Powered Research Tool), Multidisciplinary, Monthly, Online, Print Journal, Indexing in all major database & Metadata, Citation Generator, Digital Object Identifier(DOI)

Registration ID : IJSDR_ 160414

Paper ID : IJSDR1605133

Title of Paper : Leakage power reduction techniques in CMOS VLSI circuits - A Survey

Impact Factor : 9.15 (Calculate by Google Scholar) | License by Creative Common 3.0

DOI :

Published in : Volume 1 | Issue 5 | May-2016

Page No : 717 - 722

Published URL : <https://ijsdr.org/viewpaperforall.php?paper=IJSDR1605133>

Authors : D. Vijayalakshmi, Dr. P. C. Kishore Raja

Thank you very much for publishing your article in IJSDR.

Editor In Chief

International Journal of Scientific Development and Research
(ISSN: 2455-2631)

