Comparison of Enhancement of Power System Transient Stability with various Facts Devices

¹Yamala Geetha Rani, ²Madhuchandra Popuri

Department of Electrical and Electronics Engineering, Sanketika Vidya Parishad Engineering College, Visakhapatnam (Dt), A.P., India

Abstract: The modern power system has led to an increasing complexity in the study of power systems, and also presents new challenges to power system stability, and in particular, to the aspects of transient stability and small -signal stability. Transient stability control plays a significant role in ensuring the stable operation of power systems in the event of large disturbances and faults. This paper investigates the improvement of transient stability of a two-area power system, using a SVC, STATCOM, UPFC, and TCSC. The performance of UPFC is compared with other FACTS devices such as Thyristor Controlled Series Capacitor (TCSC), (STATCOM) and Static Var Compensator (SVC) respectively. The simulation results demonstrate the effectiveness and robustness of the proposed UPFC on transient stability improvement of the system.

Keywords: FACTS, Matlab/Simulink, SVC, STATCOM, UPSC, TCSC, Transient stability, Two-area power system, UPFC

I. INTRODUCTION

The power system is a complex network comprising of numerous generators, transmission lines, variety of loads and transformers. As a consequence of increasing power demand, some transmission lines are more loaded than was planned when they were built. With the increased loading of long transmission lines, the problem of transient stability after a major fault can become a transmission limiting factor [1]. Transient stability refers to the capability of a system to maintain synchronous operation in the event of large disturbances such as multi-phase short-circuit faults or switching of lines [2]. The resulting system response involves large excursions of generator rotor angles and is influenced by the nonlinear power angle relationship. Stability depends upon both the initial operating conditions of the system and the severity of the disturbance. Recent development of power electronics introduces the use of flexible ac transmission system (FACTS) controllers in power systems. FACTS controllers are capable of controlling the network condition in a very fast manner and this feature of FACTS can be exploited to improve the voltage stability, and steady state and transient stabilities of a complex power system [3]-[8].

Static VAR Compensator (SVC) is a first generation FACTS device that can control voltage at the required bus thereby improving the voltage profile of the system. The primary task of an SVC is to maintain the voltage at a particular bus by means of reactive power compensation [9]. SVCs have been used for high performance steady state and transient voltage control compared with classical shunt compensation. SVCs are also used to dampen power swings, improve transient stability, and reduce system losses by optimized reactive power control [10]-[11]. Thyristor Controlled Series Capacitor (TCSC) is one of the important members of FACTS family that is increasingly applied with long transmission lines by the utilities in modern power systems. It can have various roles in the operation and control of power systems, such as scheduling power flow; decreasing unsymmetrical components; reducing net loss; providing voltage support; limiting short-circuit currents; mitigating sub-synchronous resonance (SSR); damping the power oscillation; and enhancing transient stability [12]-14]. Among the available FACTS devices, the Unified Power Flow Controller (UPFC) is the most versatile one that can be used to improve steady state stability, dynamic stability and transient stability [16].

The UPFC can independently control many parameters since it is the combination of Static Synchronous Compensator (STATCOM). These devices offer an alternative mean to mitigate power system oscillations. It has been reported in many papers that UPFC can improve stability of single machine infinite bus (SMIB) system and multi-machine system [17]-[18]. The inter-area power system has special characteristic of stability behaviour [19]. This paper investigates the improvement of transient stability of a two-area power system with a UPFC. A Matlab/Simulink model is developed for a two-area power system with a UPFC. The performance of UPFC is compared with other FACTS devices such as SVC, TCSC, and STATCOM respectively. From the simulation results, it is inferred that UPFC is an effective FACTS device for transient stability improvement.

II. FACTS CONTROLLERS

FACTS controllers are used for the dynamic control of voltage, impedance and phase angle of high voltage AC transmission lines. The basic principles of the following FACTS controllers, which are used in the two-area power system under study, are discussed briefly.

A. Static Var Compensator (Svc)

The SVC uses conventional thyristors to achieve fast control of shunt-connected capacitors and reactors. The configuration of the SVC is shown in Fig.1.Which basically consists of a fixed capacitor (C) and a thyristor controlled reactor (L). The firing angle control of the thyristor banks determines the equivalent shunt admittance presented to the power system.



Fig.1 SVC connected to a transmission line.

B. Static Synchronous Compensator (STATCOM)

The STATCOM is based on a solid state synchronous voltage source which generates a balanced set of three sinusoidal voltages at the fundamental frequency with rapidly controllable amplitude and phase angle. The configuration of a STATCOM is shown in Fig.2. Basically it consists of a voltage source converter (VSC), a coupling transformer and a dc capacitor. Control of reactive current and hence the susceptance presented to power system is possible by variation of the magnitude of output voltage (V_{vsc}) with respect to bus voltage (V_b) and thus operating the STATCOM in inductive region or capacitive region.



Fig.2 STATCOM connected to a transmission line.

C. Thyristor Controlled Series Capacitor (TCSC)

TCSC is one of the most important and best known FACTS devices, which has been in use for many years to increase the power transfer as well as to enhance system stability. The main circuit of a TCSC is shown in Fig. 3. The TCSC consists of three main components: capacitor bank C, bypass inductor L and bidirectional thyristors SCR1 and SCR2. The firing angles of the thyristors are controlled to adjust the TCSC reactance in accordance with a system control algorithm, normally in response to some system parameter variations. According to the variation of the thyristor firing angle or conduction angle, this process can be modeled as a fast switch between corresponding reactance's offered to the power system.



Fig.3 TCSC connected to a transmission line

D. Unified Power Flow Controller (UPFC)

Among the available FACTS devices, the Unified Power Flow Controller (UPFC) is the most versatile one that can be used to enhance steady state stability, dynamic stability and transient stability. The basic configuration of a UPFC is shown in Fig. 4. The UPFC is capable of both supplying and absorbing real and reactive power and it consists of two ac/dc converters. One of the two converters is connected in series with the transmission line through a series transformer and the other in parallel with the line through a shunt transformer. The dc side of the two converters is connected through a common capacitor, which provides dc voltage for the converter operation. The power balance between the series and shunt converters is a prerequisite to maintain a constant voltage across the dc capacitor. As the series branch of the UPFC injects a voltage of variable magnitude and phase angle, it can exchange real power with the transmission line and thus improves the power flow capability of the line as well as its transient stability limit. The shunt converter exchanges a current of controllable magnitude and power factor angle with the power system. It is normally controlled to balance the real power absorbed from or injected into the power system by the series converter plus the losses by regulating the dc bus voltage at a desired value.



Fig.4 UPFC connected to a transmission line

III. TWO AREA POWER SYSTEM MODEL

Consider a two-area power system (Area-1 & Area-2) with series and shunt FACTS devices, connected by a single circuit long transmission line as shown in Fig. 5 and Fig. 6. Here, the FACTS devices such as UPFC (combination of STATCOM and SSSC), SSSC, and TCSC are equipped between bus-2 and bus-3 and the shunt FACTS device such as SVC is equipped at bus-2. The direction of real power flow is from Area-1 to Area-2. In the two-area power system model, the Area-1 consists of Generator 1 (G1) and Generator 2 (G2) and the Area-2 consists of Generator 3 (G3) and Generator 4 (G4). The system data are given in [21].



Fig.5 Two-area power system with FACTS device

IV. SIMULATION MODEL



Fig.7 Variation of generator rotor angle of the two-area power system with STATCOM

83



Fig.8 Variation of generator rotor angle of the two-area power system with SVC





V. COMPARISON BETWEEN UPFC, SVC, TCSC, AND SSSC FOR POWER SYSTEM STABILITY ENHANCEMENT

From the simulation results shown in Figs. 8,9,10. A comparison is made between the above FACTS devices for stability enhancement of two-area power system under study as shown in Table 1

Two-area Power System with	Power System Stability Enhancement	Settling time in post fault period (in seconds)
STATCOM	YES	0.4
SVC	YES	0.7
TCSC	YES	0.5
UPFC	YES	0.3



From the Table 1, it is investigated that the UPFC is the effective FACTS device for stability enhancement of inter-area power system.







Fig.12 Variation of Terminal Voltage of the two-area power system with UPFC, SVC, STATCOM and TCSC.

VI. CONCLUSION

The power system stability enhancement of a two area power system by various FACTS devices is presented and discussed. The dynamics of the system is compared with and without the presence of UPFC in the system in the event of a major disturbance. Then the performance of the UPFC for power system stability improvement is compared with the other FACTS devices such as SVC, TCSC, and STATCOM respectively. It is clear from the simulation results that there is a considerable improvement in the system performance with the presence of UPFC for which the settling time in post fault period is found to be around 0.3 second.

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